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Result # 1 Relevance:
Flexible Approach to Finite State Machines
1990-11-01 IPCOM000102394D

The approach described in this article effectively isolates the state transition and the finite state machines (FSMs). The state transition and the output generation block are in state coding ...

Result # 2 Relevance:
Low Power Sequencer Design Method
1995-01-01 IPCOM000114781D

Disclosed is a sequencer design method to lower power dissipation of the circuit. The possible while the sequencer is in the idle state. This concept can be achieved by asst hamming ...

Result # 3 Relevance:
Parity Predict for Sequencer Invalid State Transitions
1990-02-01 IPCOM000099719D

Described is a method to predict even parity for invalid state transitions, which are v changes.

Result # 4 Relevance:
Random Finite State Machines
1994-03-01 IPCOM000111621D

The Finite State Machines (FSM) of today's VLSI chips implement very complex protocols. FSMs are very difficult to exercise since they may depend on input sequences which in which several ...

Result # 5 Relevance:
Automatic State Machine Recovery When Entering Invalid States
1991-09-01 IPCOM000121492D



State machines (i.e., sequential circuit) use latches to produce a logical progression to another to create control and response signals that are used by other portions of ...



Result # 6 Relevance:
Representation of Function States in Knowledge Base
1990-12-01 IPCOM000102662D



Disclosed is a method of representing function states in the function knowledge base is desirable to have some of its aspects (the states of that function) trigger an error function.

Result # 7 Relevance:
Transition Control Statements for State Models
1995-11-01 IPCOM000116896D

In an object oriented analysis model, an active object's State Transition Diagram (S' Matrix (SEM). The SEM must be manually edited and maintained to include other state conditions. A method ...

Result # 8 **Relevance:**  
Character Support for Arabic Ligatures
1987-04-01 IPCOM000039003D
Arabic cursive script employs certain ligatures or joint characters such as lam-alef, a Shape Determination (ASD) method described in Canadian Patent 1,207,905. However, Selectric Arabic typewriter ...

Result # 9 **Relevance:**  
Choosing State Assignments to Minimize State Machine Logic
1994-06-01 IPCOM000112671D
This procedure is a simple guideline to determine the state assignments for each state and resets the state machine logic is desired. The procedure is particularly useful

Result # 10 **Relevance:**  
Representation of State Machines in Knowledge Base
1992-03-01 IPCOM000107706D
This article describes a method of representing state machines in the function knowledge

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Search query: generating a state diagram
Published Before: 1-2-2002 (Original publication date)
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